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**[PDF] Optoelectronics in Japan and the United States**

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In the entrepreneurial spirit, there is a very **compressed** time ...  
**Matsushita**, Mitsubishi, NEC, Sumitomo, and Toshiba) make the required linear lasers. ...

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**SIGDA Super Compendium, ISSS 2002, Abstracts**

103]: Satoshi **Matsushita** (NEC, Japan) ... The methodology also provides a framework for building new **trace** simulators for generic architectures. ...

[www.sigda.org/Archives/ProceedingArchives/](http://www.sigda.org/Archives/ProceedingArchives/Compendiums/papers/2002/iss02/htmlfiles/sun_sgi/issabs.htm)

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**Matsushita** ... which can predict **branch** outcomes, called. **trace** scheduling. Figure 8. ...

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Architectures. for Multimedia. TI MVP. IITVCP. NEC. VSP3. **Matsushita** ...

**branch** outcomes by applying an algorithm known as **trace** scheduling. Figure 9. ...

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Advantage: Information can be **compressed** so that all the knowledge of ...

Computer data processing system with **trace** unit for debugging in real time uses ...

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will be ... DVD uses digital video, and even though it's heavily **compressed**, ...  
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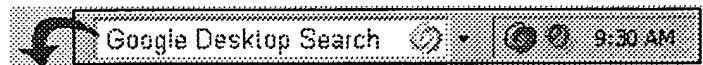
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**[PPT] Methods for Embedded Computing**

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**Selective code compression. Motivation. Branch blocks vary in size ... Branch.**

**Blocks. Profiling. Method. Selection. Compression. Compressed ...**

[www.cerc.utexas.edu/vlsi-seminar/slides/wolf-ut-04.ppt](http://www.cerc.utexas.edu/vlsi-seminar/slides/wolf-ut-04.ppt) - [Similar pages](#)

**AAPM&R - Nomenclature and Terminology for Spine Specialists**

Plain film radiographs cannot discern endplate **compression** fractures. ...

Not **selective** nerve root block. Not **selective** epidural. ...

[www.aapmr.org/passor/education/nclspine.htm](http://www.aapmr.org/passor/education/nclspine.htm) - 25k - [Cached](#) - [Similar pages](#)

**[PDF] Compressor front (Paul v1).ai**

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**Branch Office 1. Branch Office 2. compressor. compressor. optimizer. Transparent &**

**Resilient ... Selective compression by networks, hosts & applications ...**

[www.exinda.com/pdfs/Exinda\\_Compressor.pdf](http://www.exinda.com/pdfs/Exinda_Compressor.pdf) - [Similar pages](#)

**WAN Compression**

Ideal for low-speed **branch** offices it provides transparent capacity gains and

... **Selective compression**. Select the network streams to **compress** by network ...

[www.exinda.com/public/products/compressor.htm](http://www.exinda.com/public/products/compressor.htm) - 20k - [Cached](#) - [Similar pages](#)

**Instruction Stream Compression: Publications**

We also consider **selective compression** (determining which procedures in a program

... When coupled with an aggressive trace or multiple **branch** predictor, ...

[www.eecs.umich.edu/~tnm/compress/compress-pubs.html](http://www.eecs.umich.edu/~tnm/compress/compress-pubs.html) - 28k - [Cached](#) - [Similar pages](#)

**Neurosurgery - Fulltext: Volume 35(2) August 1994 p 333-334 Nerve ...**

No other cases of such **selective branch compression** have been previously reported.

Leg pain with associated calf weakness is a symptom complex most ...

[www.neurosurgery-online.com/pt/re/neurosurg/fulltext.00006123-199408000-00026.htm](http://www.neurosurgery-online.com/pt/re/neurosurg/fulltext.00006123-199408000-00026.htm) -

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**[Selective paralysis of the infraspinatus muscle, caused by ...**

**[Selective paralysis of the infraspinatus muscle, caused by compression of ...**

After careful examination an isolated **compression** of the inferior **branch** of ...

[www.ncbi.nlm.nih.gov/entrez/query.fcgi?cmd=Retrieve&](http://www.ncbi.nlm.nih.gov/entrez/query.fcgi?cmd=Retrieve&db=PubMed&list_uids=3508338&dopt=Abstract)

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**EP0929173**

3 is a block diagram illustrating a **selective compression** network access server

... 4 is a flow diagram of a method for **selective compression** of voice ...

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**[PDF] LZW-Based Code Compression for VLIW Embedded Systems**

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on different **branch** blocks. We propose two **selective compression** schemes based on LZW **compression**. As shown in Figure 4, we collect ...

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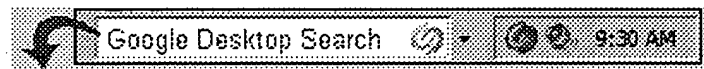
If **compression** occurs after the nerve bifurcates, **selective** paralysis of muscles

... **Compression** of the medial **branch** causes paralysis of the extensor carpi ...

www.emedicine.com/orthoped/ byname/radial-nerve-entrapment.htm - 17k - [Cached](#) - [Similar pages](#)

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### Mache: No-Loss Trace Compaction

AD Samples - SIGMETRICS, 1989 - [portal.acm.org](http://portal.acm.org)... as well as the amount of space required to **store the trace** ... contains the last **address** read from the **trace** for each ... it is initialized to zero at the **start** of the ...Cited by 42 - [Web Search](#) - [portal.acm.org](http://portal.acm.org) - [Library Search](#)

### The SimpleScalar tool set, version 2.0

D Burger, TM Austin, S Bennett - ACM SIGARCH Computer Architecture News, 1997 - [portal.acm.org](http://portal.acm.org)... level debugger (DLite!) and a tool to **trace** the out ... 00000 **Start** of text segmentOx!000000 **Start** of data ... Load/Store lb - load byte lbu - load byte unsigned lh ...Cited by 651 - [Web Search](#) - [cs.ucr.edu](http://cs.ucr.edu) - [ee.pdx.edu](http://ee.pdx.edu) - [lems.brown.edu](http://lems.brown.edu) - [all 81 versions](#) »

### Heads and tails: a variable-length instruction format supporting parallel fetch and decode

H Pan, K Asanovic - CASES, 2001 - [portal.acm.org](http://portal.acm.org)... In contrast to earlier schemes that **store** compressed variable ... bytes of an instruction before the **start address** of the ... length micro-ops in a **trace** cache, but ...Cited by 7 - [Web Search](#) - [ai.mit.edu](http://ai.mit.edu) - [ai.mit.edu](http://ai.mit.edu) - [portal.acm.org](http://portal.acm.org)

### Designing a Trace Format for Heap Allocation Events

TM Chilimbi, RE Jones, BG Zorn - ISMM, 2000 - [cs.purdue.edu](http://cs.purdue.edu)... For example, it would be wasteful to **store** full type ... Table 8: Effectiveness of **Address** Encodings meta-language for **trace** format specification, Meta-TF, by ...Cited by 10 - [View as HTML](#) - [Web Search](#) - [cs.kent.ac.uk](http://cs.kent.ac.uk) - [cs.ukc.ac.uk](http://cs.ukc.ac.uk) - [portal.acm.org](http://portal.acm.org) - [all 8 versions](#) »

### Executing compressed programs on an embedded RISC architecture

A Wolfe, A Chanin - Proceedings of the 25th annual international symposium on ..., 1992 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)... code compression and in-cache expansion in the **TRACE** 7/200 ... Ditzelgl] uses a Decoded Instruction Cache to **store** up to ... cache line such that it must **start** on an ...Cited by 128 - [Web Search](#) - [portal.acm.org](http://portal.acm.org) - [portal.acm.org](http://portal.acm.org) - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

### Tools for characterizing distributed shared memory applications

GA Abandah - 1996 - [hpl.hp.com](http://hpl.hp.com)... SMAIT additionally instruments all load and **store** instructions to ... access instructions are not represented in the **trace**. ... a block between a procedure **start** or a ...Cited by 5 - [View as HTML](#) - [Web Search](#) - [eecs.umich.edu](http://eecs.umich.edu) - [hpl.hp.com](http://hpl.hp.com) - [Library Search](#)

### Instruction fetch mechanisms for VLIW architectures with compressed encodings

TM Conte, S Banerjia, SY Larin, KN Menezes, SW ... - PROC ANNU INT SYMP MICROARCHITECTURE, 1996 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)... organizations are described and evaluated through **trace**-driven simula ... are compressed encodings, which do not **store** NOPs ... the new quantity (NextPC) to **address** the i ...

